

HND ASSIGNMENT EVIDENCE AND FEEDBACK



Learning Outcome	Evidence in Report (Student)	Location (Student)	Assessor Comments	Referred	Achieved
LO3.1					
LO3.2					
LO3.3					
Merit					
M1					
M2					
Distinction					
D2					

Assessment Front Sheet Year 2016/17



Programme Title HND Electronic Engineering	Module/Unit Unit 117 Digital and Analogue Devices and Circuits F/602/2237	Level QCF Level 5	Year 2				
Assessor: Lead Assessor: IV/IM:	Assessment type: Investigation	Date issued: Submission date:	Date received:				
Student name:	Summary of Outcomes:						
	Assessed	LO3.1	LO3.2	LO3.3	M1	M2	D2
	Awarded						
Assessment title: Digital Electronic Circuits		Assignment ref: HNDDADC3					
Summary of assessment decision							
FOR DETAILED FEEDBACK SEE NOTES IN SCRIPT AND ON REAR PAGE							
Student comment on assessment decision							
Assessment brief Internally Verified				This Assessment was sampled as part of the Programme IV/IM process:			
IV/IM Signature:		Date:		IV/IM Signature:		Date:	
Signature of Assessor:				Signature of EV:			
Date:				Date:			